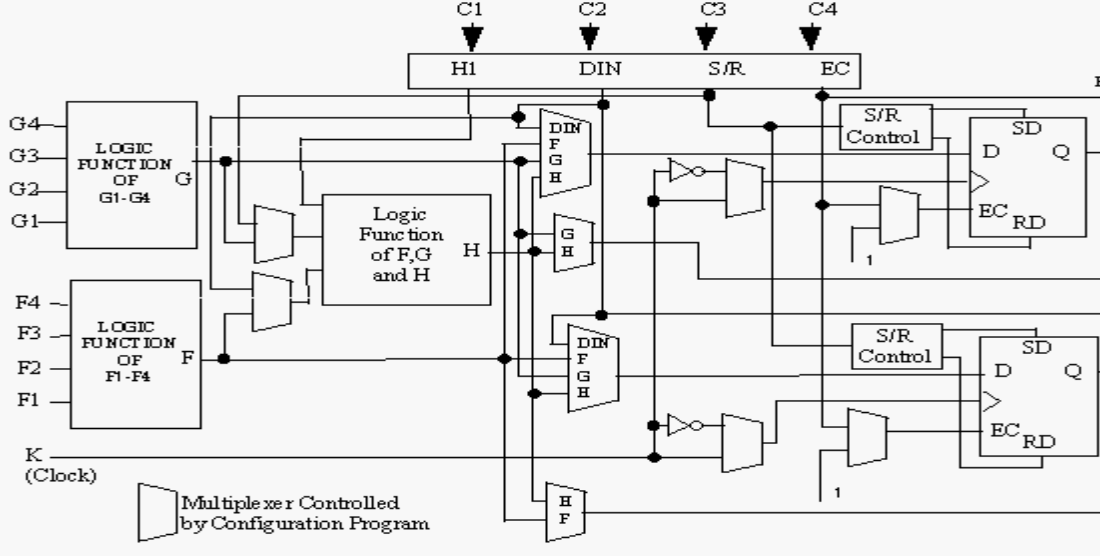
C1
C2
C3
C4
H1
DIN
S/R
EC
G4
G3
G2
G1
LOGIC FUNCTION OF G1-G4
G
Logic Function of F,G and H
H
F4
F3
F2
F1
LOGIC FUNCTION OF F1-F4
F
K
(Clock)
DIN
F
G
H
S/R Control
SD
D
Q
EC
RD
1
Multiplexer Controlled by Configuration Program